

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
Sang Yoon LEE et al)	Group Art Unit: Unassigned
Application No.: Unassigned)	Examiner: Unassigned
Filed: February 3, 2004)	Confirmation No.: Unassigned
For: ORGANIC THIN FILM TRANSISTOR)	
COMPRISING MULTI-LAYERED GATE)	
INSULATOR)	
)	
)	
)	

FIRST INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

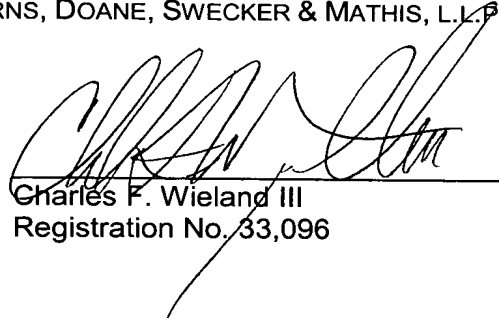
In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date February 3, 2004

By: 
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Substitute for form 1449A/PTO & 1449B/PTO			C mplete if Kn wn		
FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number	Unassigned	
			Filing Date	February 3, 2004	
			First Named Inventor	Sang Yoon LEE et al	
			Examiner Name	Unassigned	
Sheet	1	of	1	Attorney D cket Number	021269-009

U.S. PATENT DOCUMENTS				
Examiner Initials	Document Number	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Issue/Publication Date (MM-DD-YYYY)
	5,981,970		Dimitrakopoulos et al.	11-09-1999
	6,232,157		Dodabalapur et al.	05-15-2001
	6,558,987		Lee	05-06-2003
	6,563,174		Kawasaki et al.	05-13-2003

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation	
					Yes	No

NON-PATENT LITERATURE DOCUMENTS	
Examiner Initials	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	C. D. DIMITRAKOPOULOS et al.; Low-Voltage Organic Transistors on Plastic Comprising High-Dielectric Constant Gate Insulators; Report; February 5, 1999; pages 822-824; Vol. 283; Science, USA.
	Y.Y. LIN et al.; High-Mobility Pentacene Organic Thin Film Transistors; 54 th Annual Device Research Conference Digest; 1996; pages 80-81.

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.